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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

8/ Inve Application of

Michio MORITA

Serial No.: 10/628,451

Group Art Unit:

Filed: July 29, 2003

Examiner:

For: METHOD FOR FABRICATING MULTILAYER INTERCONNECT AND METHOD
FOR CHECKING THE SAME

PRELIMINARY AMENDMENT

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Prior to examination of the above-referenced application, please amend the application as

follows:

IN THE BRIEF DESCRIPTION OF THE DRAWINGS: